EENG 284

–

Digital Design Lab

How To: Create a Project

**Creating a Project**

1. Select an appropriate working directory for your project. I would recommend selecting your network drive.
   1. Create a new folder for your project *projectFolder*,
   2. Download any provided Verilog files into *projectFolder*,
2. Start Quartus II 18.1 (64-bit).
3. Select *File -> New Project Wizard.*
4. In the **Directory, Name, Top-Level Entity** page of the New Project Wizard pop-up:
   1. To the right of the “What is the working directory” box click the … button,
   2. In the Select Folder pop-up, navigate so you can see the andgate2 directory created in step 1,
   3. Select the *projectFolder* folder, click Select Folder,
   4. In the “What is the name of this project” field type *projectName*
   5. click *Next*.
5. In the **Project Type** page of the New Project Wizard pop-up:
   1. Select the *Empty project* radio button,
   2. click *Next*.
6. If you have given Verilog files, in the **Add Files** page of the New Project Wizard pop-up:
   1. Click the … button to the right of File name,
   2. In the Select File pop-up, navigate to, and select, *projectFiles.v*, click Open,
   3. The file should appear in the window below,
   4. Click *Next*
7. In the **Family & Device Settings** page of the New Project Wizard pop-up:
   1. Device family, Family: Cyclone V
   2. Package: FBGA
   3. Pin Count: 672
   4. Speed Grade: 7\_H6
   5. Select *Specific device selected in ‘Available devices’ list*
   6. From the list of available devices, select: 5CGXFC5C6F27C7
   7. Click *Next*
8. In the **EDA Tool Settings** page of the New Project Wizard pop-up:
   1. In the Simulation row
      1. Tool Name column: ModelSim-Altera
      2. Formats column: Verilog HDL
   2. Leave other defaults alone
   3. Click Next
9. In the **Summary** page of the New Project Wizard pop-up:
   1. Review information,
   2. Click Finish.
10. Back in the main Quartus II window, Click *Tools -> Options..*.
11. In the Options pop-up:
    1. Select *EDA Tool Options* from the Category menu,
    2. If the last row, “ModelSim-Altera” is blank, click on the … button at right and navigate to the *C:\intelFPGA\_lite\18.1\modelsim\_ase\*,select the *win32aloem* folder, the click Select Folder,
    3. Click Ok.